ECE481/581 Lab and Project Report Evaluation Rubric

The following rubric will be used in grading all laboratory and project reports submitted for this course. There are five categories under which your reports will be graded including design and VHDL description, design demonstration, overall laboratory report not including figures and tables, figures and tables, and test/simulation documentation. Each category will be assigned a specific rubric score (0-4). Rubric scores will be multiplied by a weighting factor for each category to determine the final score for the report.

Design and VHDL (50% of total lab/project score)
The Design and VHDL category will be graded using the ECE480 VHDL Code Evaluation Rubric given on the course website. For multiple designs, each design will count equally toward the total.

Design Demonstration (15% of total lab/project score)
The Design Demonstration will be graded using a Lab Demonstration sheet appropriate to each lab. For multiple designs, each design demonstration will count equally toward the total.

Overall Laboratory/Project Report not including Figures and Tables (20% of total score)	**Rubric Score**
The report is extremely well organized, properly formatted according to IEEE publication standards, and easy to follow. Discussion in each report section is relevant and complete. There are no grammar or spelling errors.	4
The report is reasonably easy to read and complete. There are minor formatting and/or grammar problems.	3
The report is readable only with significant effort. There are significant sections in the report either missing or significantly incomplete. There are major formatting and/or grammar problems.	2
The report is poorly organized and difficult to read. There is little effort to address basic report requirements.	1
Only a minimal effort is made for the majority of the sections within the report.	0

Figures and Tables (5% of total score)	**Rubric Score**
All figures and/or tables are complete, detailed, properly formatted according to IEEE publication standards and readable. Figures and/or tables provide useful information in a logical format. Captions are completely consistent with the associated figure/table. There are no grammar or spelling errors.	4
Figures and/or tables are mostly complete. Captions are consistent with the associated figure/table but could be improved. There are minor formatting or detail omissions that would have improved understanding of the purpose of the figure/table. There may be limited grammar or spelling errors. IEEE publication standards are not completely followed for one or more figures or tables.	3
The figure or table is incomplete or some portions are not readable. There are many spelling and/or grammar errors that detract from the figure/table.	2
The figure or table is mostly incomplete or most portions are not readable. There are a significant number of spelling and/or grammar errors that detract from the figure/table.	1
The figure/table is completely unreadable or provides no useful information. IEEE publication standards are ignored.	0

Test, Simulation and Documentation (10% of total score)	**Rubric Score**
The design test process is extremely well documented. Documentation describes tests that were performed, anticipated results, and observed results. A justification is given for all tests performed. Debugging and corrections to the design are described with respect to how conducted tests revealed design errors.	4
The design test process is documented with minor omissions. Documentation does not describe all tests that were performed, anticipated results, and observed results. Some test justifications are omitted. Only some debugging and corrections to the design are described.	3
The design test process is documented with a number of omissions. Documentation describes few tests that were performed, anticipated results are not discussed, or observed results are omitted. Little test justifications are given. Little to no debugging and corrections to the design are described.	2
The design test process is documented with a significant number of omissions. Documentation describes few tests that were performed, anticipated results are not discussed, or observed results are omitted. No test justifications are given. No debugging and corrections to the design are described.	1
The design was not tested or the tests were not documented.	0